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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Berger, Alexander J.; Kretz, Frank E.
Assignee: Tru-Si Technologies, Inc.
Title: Alignment of Semiconductor Wafers And Other Articles
Serial No.: 09/905,218 Filing Date: July 13, 2001
Examiner: Krizek, Janice Lee Group Art Unit: 3652
Docket No.: M-11882 US

San Jose, California
October 15, 2002

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

REPLY TO RESTRICTION REQUIREMENT

Dear Sir:

The Office Action dated September 25, 2002 requires a restriction to one of the following groups of claims:

- I. Claims 1-12 and 21-24;
- II. Claims 13-20.

Applicants elect Claims 13-20.

The undersigned can be contacted by telephone at the number below with any questions regarding this case.

EXPRESS MAIL LABEL NO:
EV 174 806 967 US

Respectfully submitted,

Michael Shenker

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